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Substitute for form 1449A/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Application Number	10/647,605
		Filing Date	August 25, 2003
		First Named Inventor	S. Brandon Keller
		Art Unit	
		Examiner Name	
Sheet 1 of 1	Attorney Docket Number	100111260-1	

U.S. PATENT DOCUMENTS					
Examiner Initials *	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
MTD		US-2002/040901 A1	02-06-2001	DARDEN ET AL	
		US- 6,185,722 B1			
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FOREIGN PATENT DOCUMENTS						
Examiner Initials *	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				
MTD		JP 07334532 A	12/22/1995	Mitsubishi Denki Semiconductor		

Examiner Signature		Date Considered	09/09/05
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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p. 83/412

PATENT APPLICATION

Sheet 1 of 6



FORM PTO-1449

LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S INFORMATION DISCLOSURE
STATEMENT

(Use several sheets if necessary)

ATTY. DOCKET NO.

100111260-1

APPLICATION NO.

10/647,605

CONFIRMATION NO.

APPLICANT

S. Brandon Keller, et al.

FILING DATE

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GROUP

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
		1A			
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FOREIGN PATENT DOCUMENTS

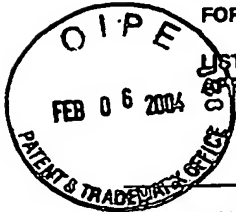
		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
		1L				
		1M				
		1N				
		1O				
		1P				

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

1Q	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,682, Entitled: Method And Program Product For Determining Nets Requiring Detailed Electromigration And Self Heating Analysis In A Digital Integrated Circuit; Filed November 12, 2003
1R	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,698, Entitled: Method And Program Product For Performing Self-Heating Analysis In A Digital Integrated Circuit Through A Single Cycle Transient Simulation; Filed November 12, 2003
1S	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,376, Entitled: Method And Program For Visual Display and One-Click Repair Of Electromigration And Self Heating Design-Rule Violations In A Digital Integrated Circuit Layout Database; Filed November 12, 2003

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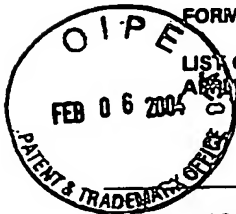
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

11-7	2Q	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,501, Entitled: Method And Program Product For Performing Electromigration Analysis In A Digital Integrated Circuit By Converting A Netlist To A DC Model And Performing DC Analysis Of The DC Model; Filed November 12, 2003
	2R	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,526, Entitled: Method And Program Product For Performing A Worst Case Electromigration And Self Heating Analysis In A Digital Integrated Circuit With Worst-Case Superposition Of Partial Currents; Filed November 12, 2003
	2S	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,692, Entitled: Method And Program Product For Performing Electromigration Analysis In A Digital Integrated Circuit Through A Single Cycle Transient Simulation; Filed November 12, 2003

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	3L				
	3M				
	3N				
	3O				
	3P				

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

3Q	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,508, Entitled: Method And Program Product For Performing Self-Heating Analysis In A Digital Integrated Circuit Layout Database by Substituting Resistive Models For Active Devices; Filed November 12, 2003
3R	KELLER, S. Brandon; ROGERS, Dennis R.; ROBERTS, George H.; U.S. Patent Application filed under EV210655516US; Entitled: Method And Program Product For Determining Worst Case Currents In A Digital Integrated Circuit Through Worst-Case Superposition Of Partial Currents; Docket No.: 200300123-1; Filed January 30, 2004
3S	KELLER, S. Brandon; ROGERS, Dennis R.; ROBERTS, George H.; U.S. Patent Application filed under EV210655564US; Docket: 200308901-1; Entitled: Systems And Methods For Re-Using Circuit Design Analysis Results; Filed January 30, 2004

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	4L				
	4M				
	4N				
	4O				
	4P				

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

MTD	4Q	KELLER, S. Brandon; ROGERS, Dennis R.; ROBERTS, George H.; U.S. Patent Application filed under EV210655581US; Docket No.: 200308899-1; Entitled: System And Method For Determining Detail Of Analysis In A Circuit Design; Filed January 30, 2004
	4R	KELLER, S. Brandon; ROGERS, Dennis R.; ROBERTS, George H.; U.S. Patent Application filed under EV210655595US; Docket No.: 200308898-1; Entitled: Systems And Methods That Identify Equivalent Instantiation-Specific Configuration Information For Analysis Tools; Filed January 30, 2004
	4S	KELLER, S. Brandon; ROGERS, Dennis R.; ROBERTS, George H. & STEVENS, Scott Alan; U.S. Patent Application filed under EV210655520US; Entitled: System And Method To Limit Analyzed Current Flow In A Circuit Design; Filed January 30, 2004

EXAMINER

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FORM PTO-1449

ATTY. DOCKET NO.

10011260-1

APPLICATION NO.

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	5J			
	5K			

FOREIGN PATENT DOCUMENTS

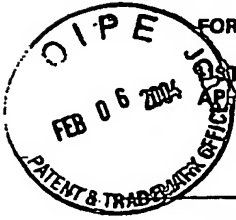
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	5L				
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	5P				

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

M-7)	5Q	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERTS, George H.; U.S. Patent Application filed under EV21065553US; Docket: 200310172-1; Entitled: System And Method For Processing Configuration Information; Filed January 30, 2004
	5R	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERTS, George H.; U.S. Patent Application filed under EV21065555US; Docket: 200310170-1; Entitled: System And Method For Balancing Run-Time And Result Accuracy In A Circuit Design Analysis Tool; Filed January 30, 2004
	5S	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERTS, George H.; U.S. Patent Application filed under EV21065557US; Docket: 200308900-1; Entitled: System And Method For Indicating Logic State Combinations Used During Circuit Design Analysis; Filed January 30, 2004

EXAMINER

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FORM PTO-1449

 OFFICE OF PATENTS AND PUBLICATIONS FOR
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	6L				
	6M				
	6N				
	6O				
	6P				

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

6Q	KELLER, S. Brandon; ROGERS, Dennis R.; ROBERTS, George H.; U.S. Patent Application filed under EV210655547US; Docket: 200310171-1; Entitled: System And Method For Determining Control Signal Combinations For Use During Simulation Of A Stage Of A Circuit Design; Filed January 30, 2004
6R	
6S	

EXAMINER

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet	1	of	1
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Complete if Known

Application Number	10/647,605
Filing Date	August 25, 2003
First Named Inventor	S. Brandon Keller
Art Unit	Not Yet Assigned
Examiner Name	Magid Y. Dimyan
Attorney Docket Number	100111260-1

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
M-10		US-2002/0144219	10-03-2002	Zachariah, et al.	
		US- 2003/0200519	10-23-2003	Argyres	
		US- 2003/0208721	11-06-2003	Regnier	
		US- 2003/0221173	11-27-2003	Fisher	
		US- 5,249,133	09-28-1993	Batra	
		US- 5,301,318	04-05-1994	Mittal	
		US- 5,688,732	09-16-1997	Khouja, et al.	
		US- 5,673,420	09-30-1997	Reyes, et al.	
		US- 5,682,320	10-28-1997	Khouja, et al.	
		US- 5,696,694	12-09-1997	Khouja, et al.	
		US- 5,838,579	11-17-1998	Olson, et al.	
		US- 5,903,476	05-11-1999	Mauskar, et al.	
		US- 5,946,218	08-31-1999	Taylor, et al.	
		US- 5,949,691	09-07-1999	Kurosaka, et al.	
		US- 6,075,932	08-13-2000	Khouja, et al.	
		US- 6,272,671	08-07-2001	Fakhry	
		US- 6,330,703	12-11-2001	Saito, et al.	
		US- 6,345,379	02-05-2002	Khouja, et al.	
		US- 6,378,123	04-23-2002	Dupenloup	
		US- 6,490,717 B1	12-03-2002	Pederson, et al.	
		US- 6,483,864	12-10-2002	Liu	
		US- 6,529,861	03-04-2003	Patra, et al.	
		US- 6,531,923 B2	03-11-2003	Burns	
		US- 6,598,211	07-22-2003	Zachariah, et al.	
		US- 6,751,782 B2	06-15-2004	Levin, et al.	
		US- 6,801,884	10-05-2004	Ferrer, et al.	
		US- 6,836,877	12-28-2004	Dupenloup	

FOREIGN PATENT DOCUMENTS

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**Examiner
Signature**

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